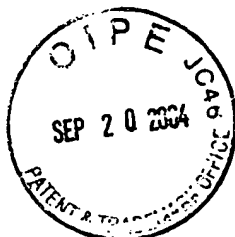


S/N 09/637,532



PATENT

2124  
41

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant:	Dmitriy Rumynin et al.	Examiner:	David H. Malzahn
Serial No.:	09/637,532	Group Art Unit:	2124
Filed:	August 11, 2000	Docket No.:	1365.033US1
Title:	A PARALLEL COUNTER AND MULTIPLICATION LOGIC CIRCUIT		

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**AMENDMENT AND RESPONSE UNDER 37 CFR § 1.111**

MS Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RECEIVED**  
SEP 22 2004  
**Technology Center 2100**

This responds to the Office Action mailed on March 15, 2004. Please amend the above-identified patent application as follows.

This response is accompanied by a Petition, as well as the appropriate fee, to obtain a three-month extension of the period for responding to the Office Action, thereby moving the deadline for response from June 15, 2004 to September 15, 2004.